

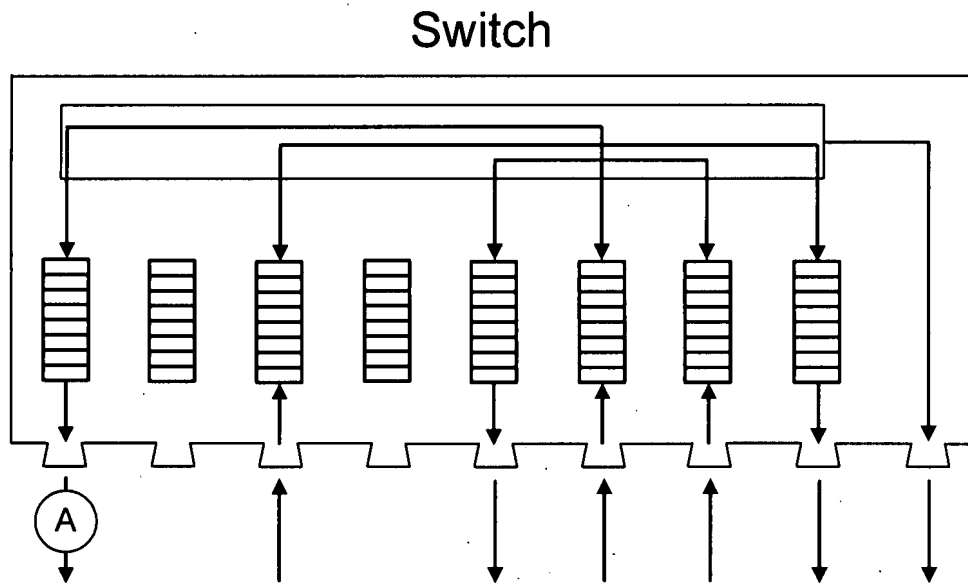


"Method to Associate Input and Output Interfaces with Packets Read from a Mirror Port"

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Application Serial No: 09/745,260

1/7



Special switch port set up for port aliasing.
This port is tied directly to the switch backbone

Figure 1
(PRIOR ART)

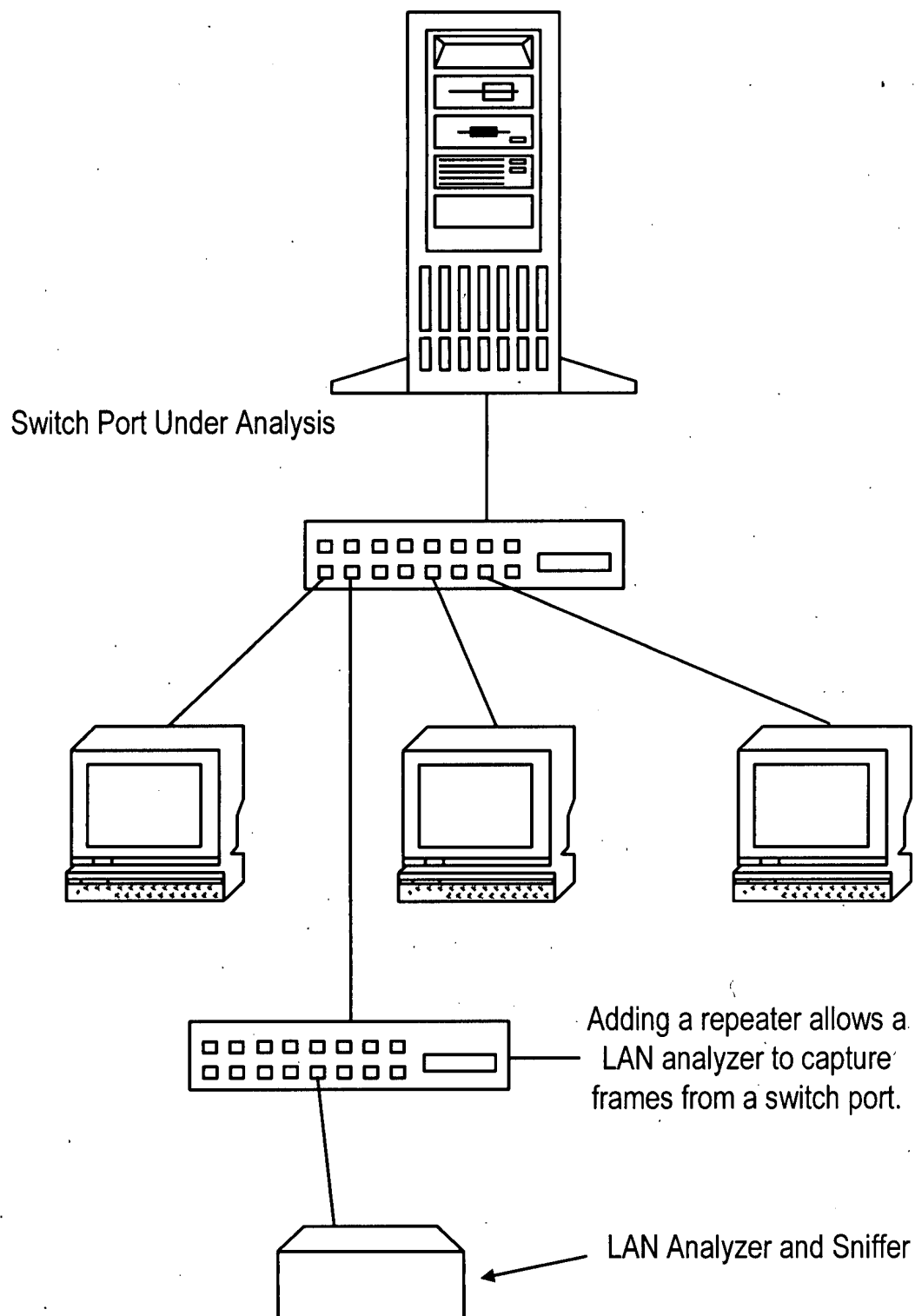


Figure 2 (PRIOR ART)

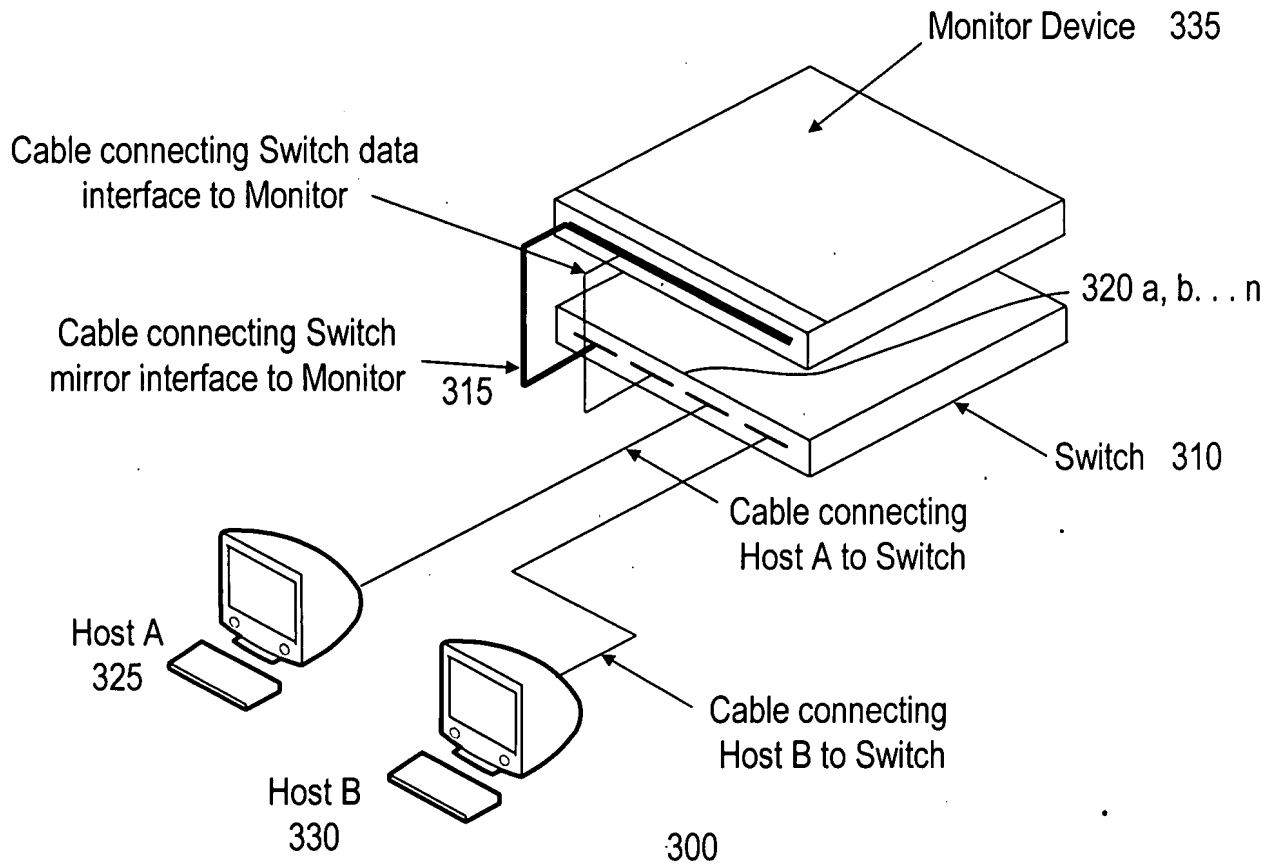


Figure 3

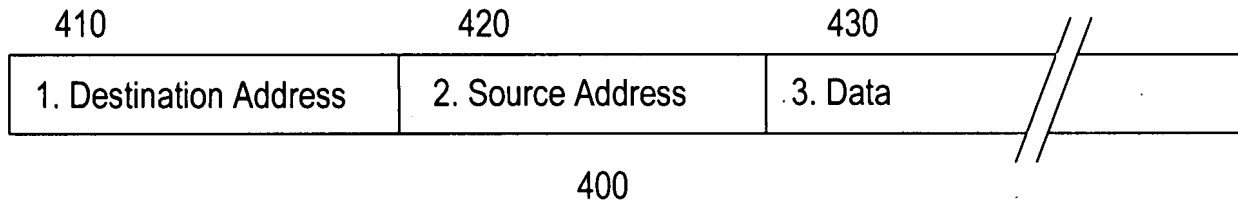


Figure 4

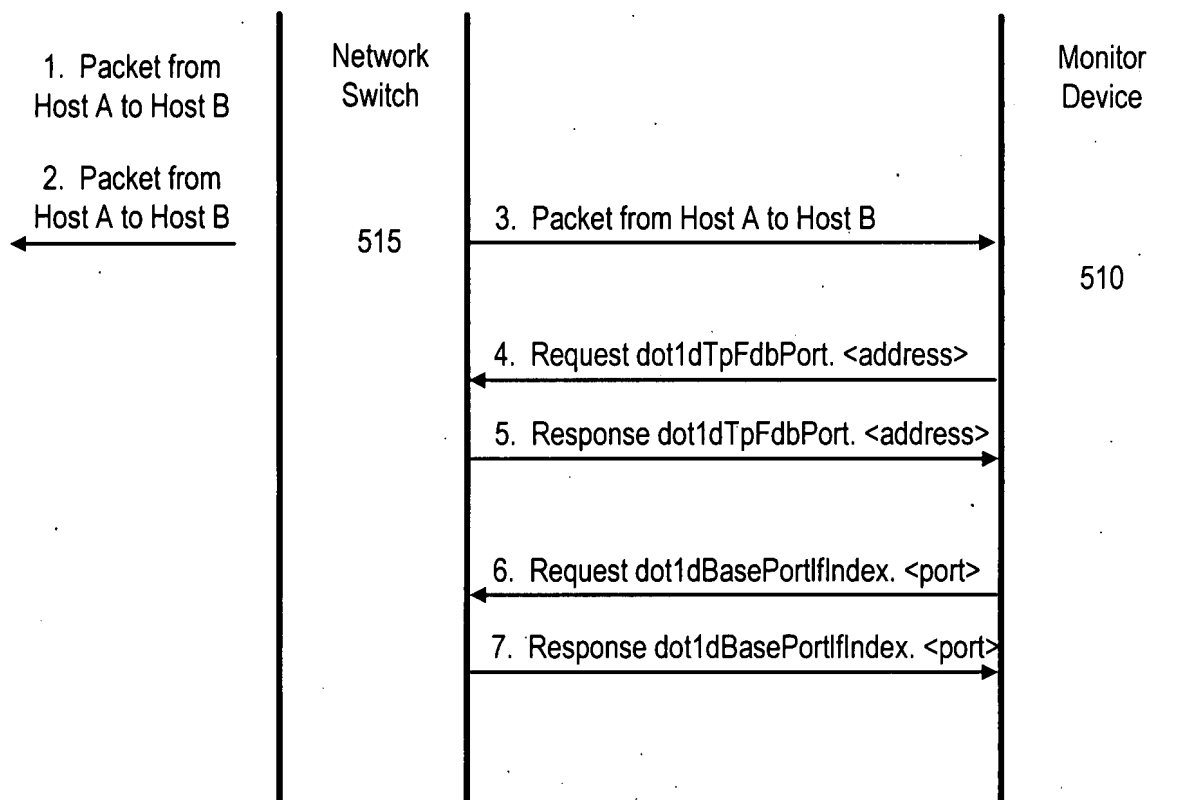


Figure 5

Flow Chart - Receiving a Packet from the Mirror Port

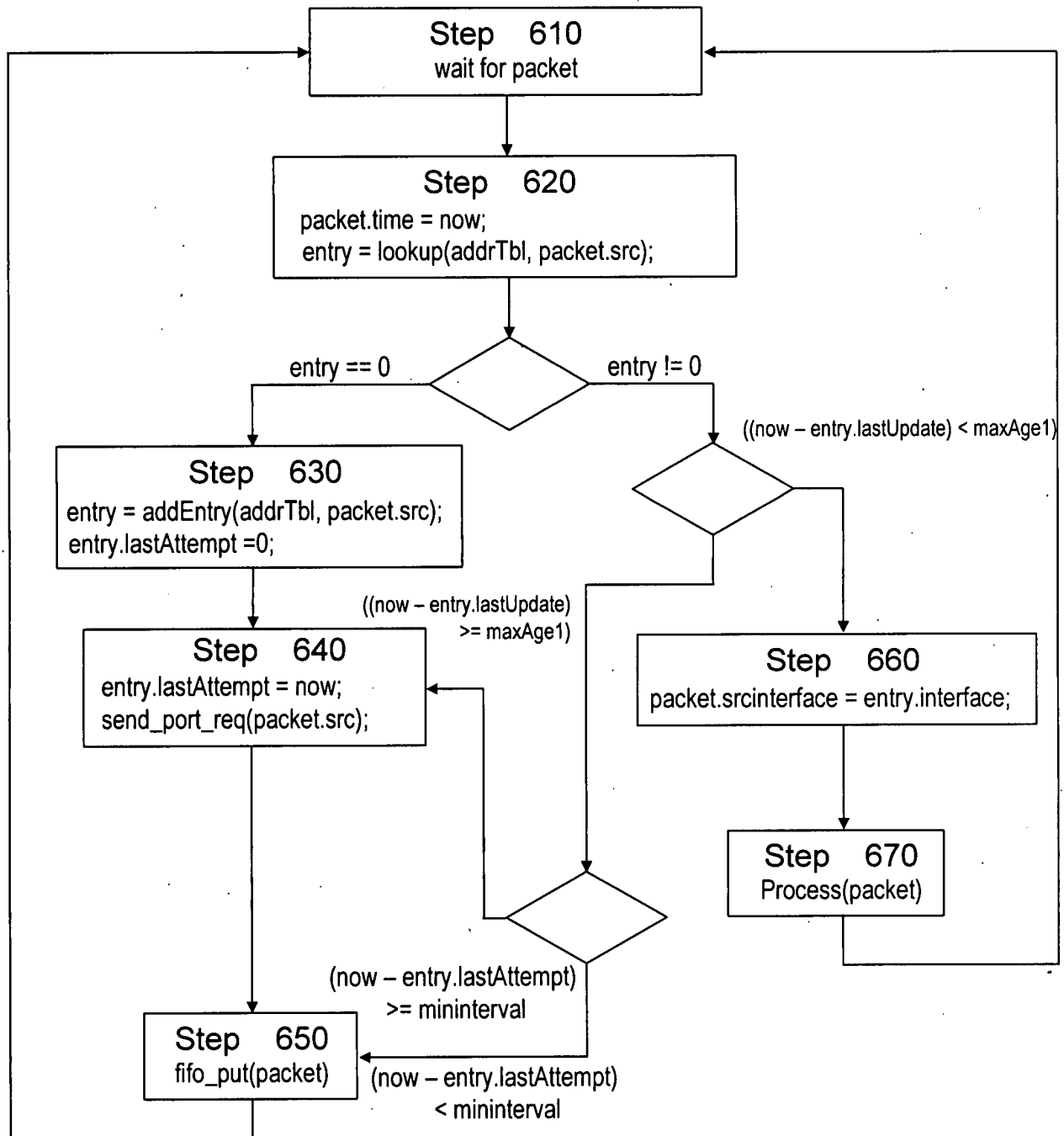


Figure 6

Flow Chart - Receiving an SNMP Response

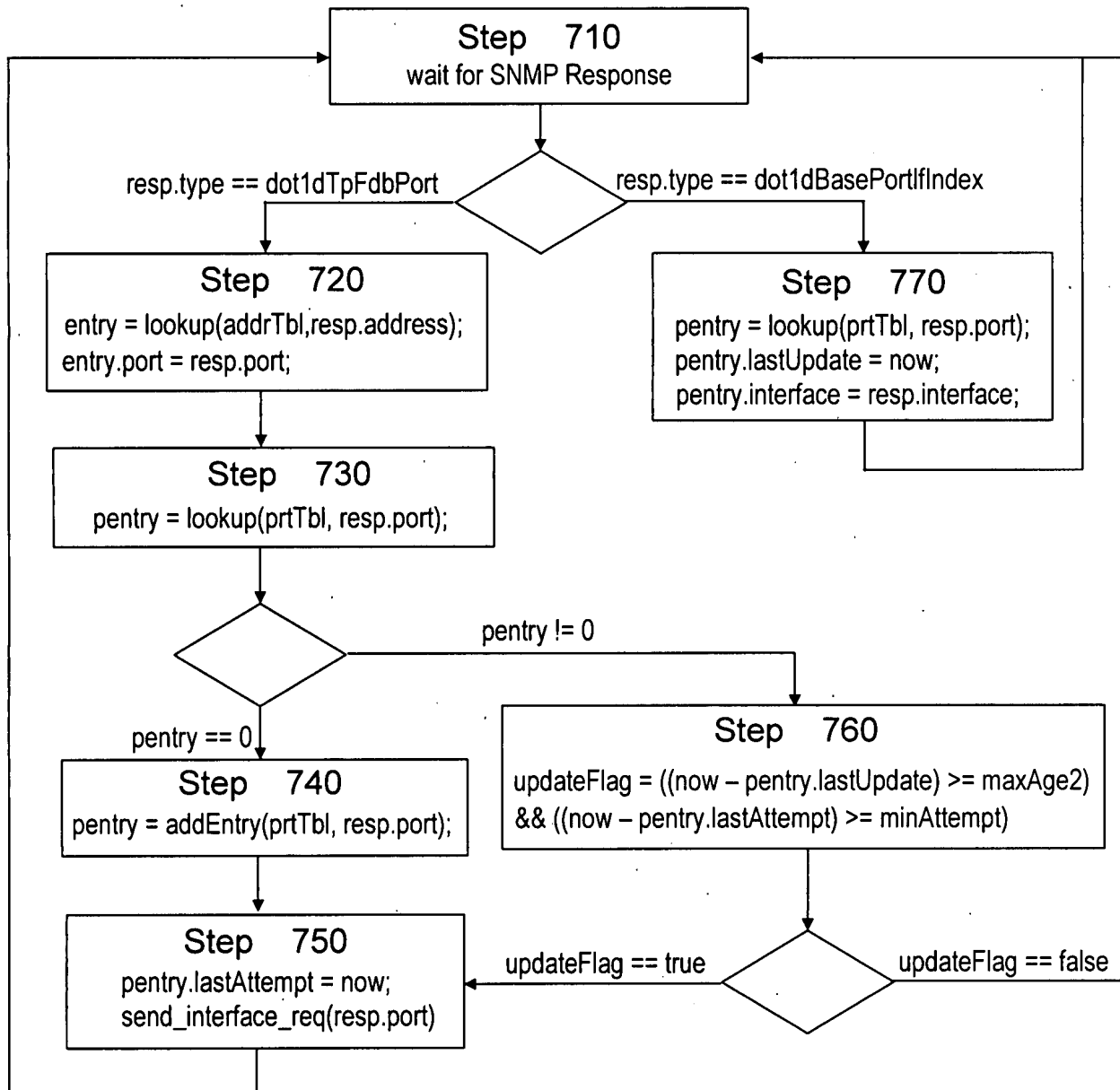


Figure 7

Flow Chart – Removing Packets from FIFO

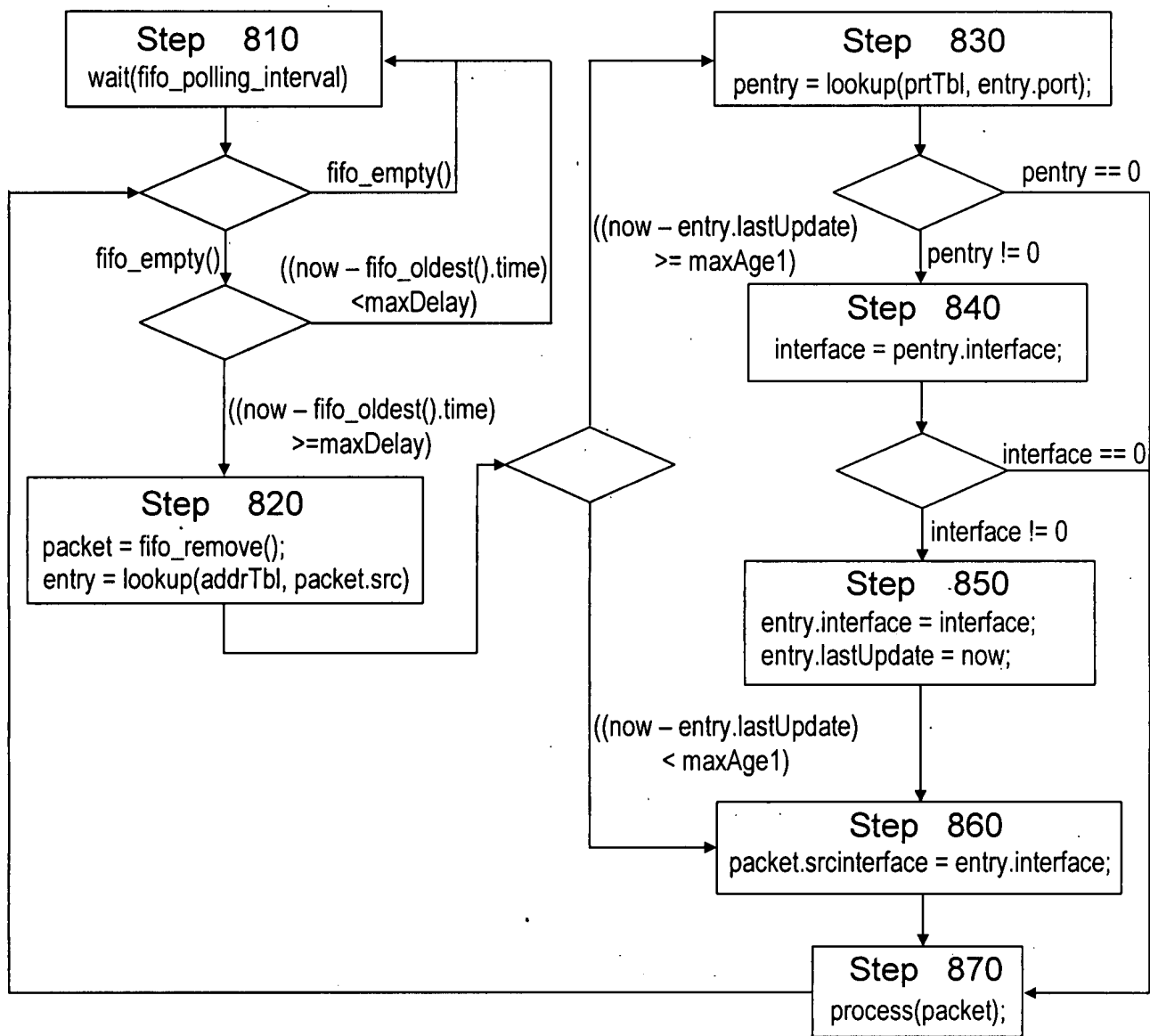


Figure 8